



YEA SHIN TECHNOLOGY CO., LTD

YS3903ZBB

P-Channel Enhancement MOSFET

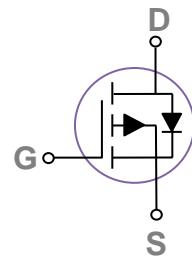
V_{DS}= -30V, ID= -50A



Features

- -30V, -50A, R_{DSON} = 8.5mΩ@V_{GS} = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

PPAK3x3 Pin Configuration



Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings T_c=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage	±10	V
I _D	Drain Current – Continuous (T _c =25°C)	-50	A
	Drain Current – Continuous (T _c =100°C)	-32	A
I _{DM}	Drain Current – Pulsed ¹	-200	A
P _D	Power Dissipation (T _c =25°C)	59	W
	Power Dissipation – Derate above 25°C	0.47	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	2.1	°C/W

DEVICE CHARACTERISTICS

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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	---	-0.03	---	$^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
$I_{\text{GS}}^{\text{SS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS}(\text{ON})}$	Static Drain-source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_D=-10\text{A}$	---	7.1	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-8\text{A}$	---	11.5	14	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
			---	4	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_D=-10\text{A}$	---	14	---	S

Dynamic and Switching Characteristics

Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_D=-10\text{A}$	---	35	56	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	10.8	16	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	10.6	16	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=6\Omega$, $I_D=-1\text{A}$	---	24.5	38	ns
T_r	Rise Time ^{2,3}		---	10.5	16	
$T_{\text{d}(\text{off})}$	Turn-On Delay Time ^{2,3}		---	156.8	230	
T_f	Fall Time ^{2,3}		---	50	75	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3300	4800	pF
C_{oss}	Output Capacitance		---	410	700	
C_{rss}	Reverse Transfer Capacitance		---	280	500	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1\text{MHz}$	---	8.5	12	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-50	A
I_{SM}	Pulsed Source Current		---	---	-100	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

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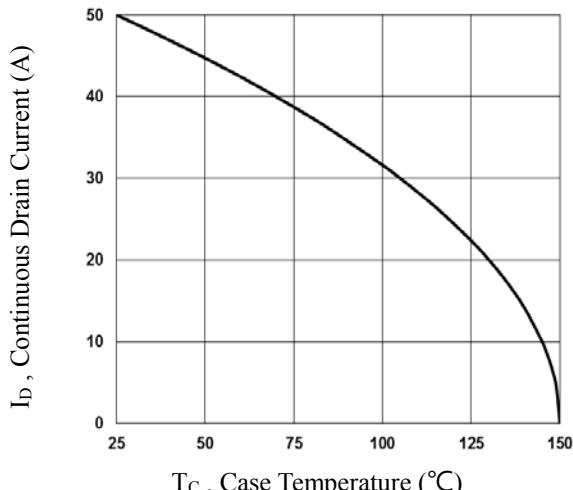


Fig.1 Continuous Drain Current vs. T_c

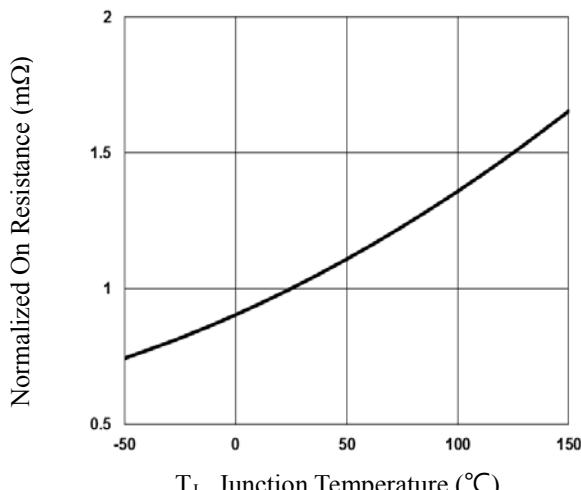


Fig.2 Normalized RDS(on) vs. T_j

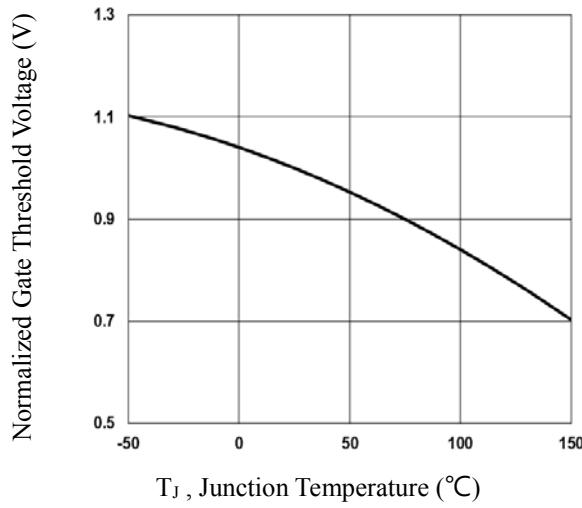


Fig.3 Normalized V_{th} vs. T_j

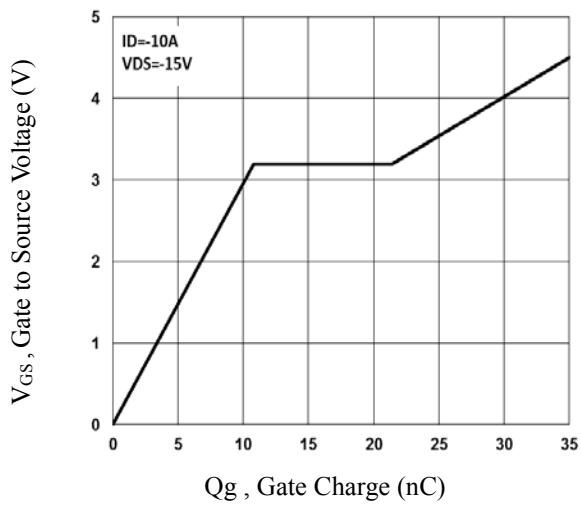


Fig.4 Gate Charge Waveform

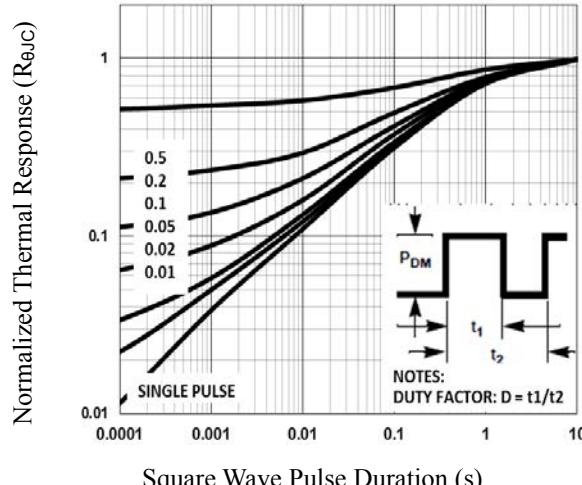


Fig.5 Normalized Transient Impedance

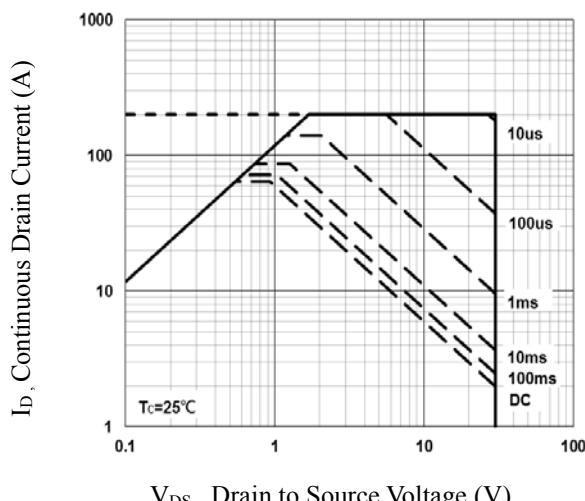


Fig.6 Maximum Safe Operation Area

DEVICE CHARACTERISTICS

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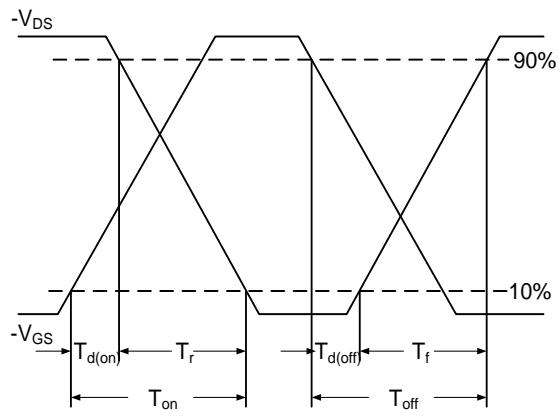


Fig.7 Switching Time Waveform

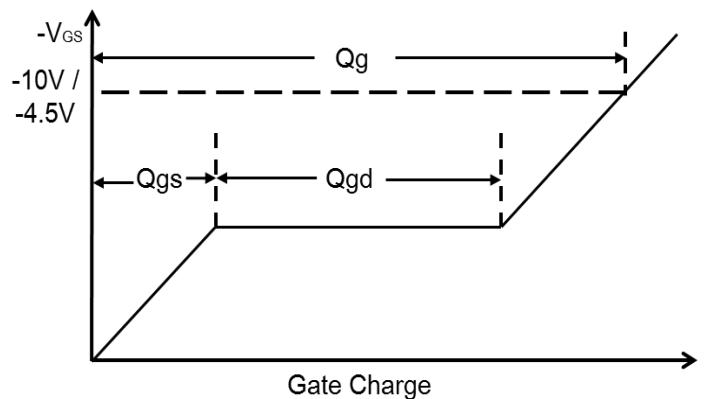
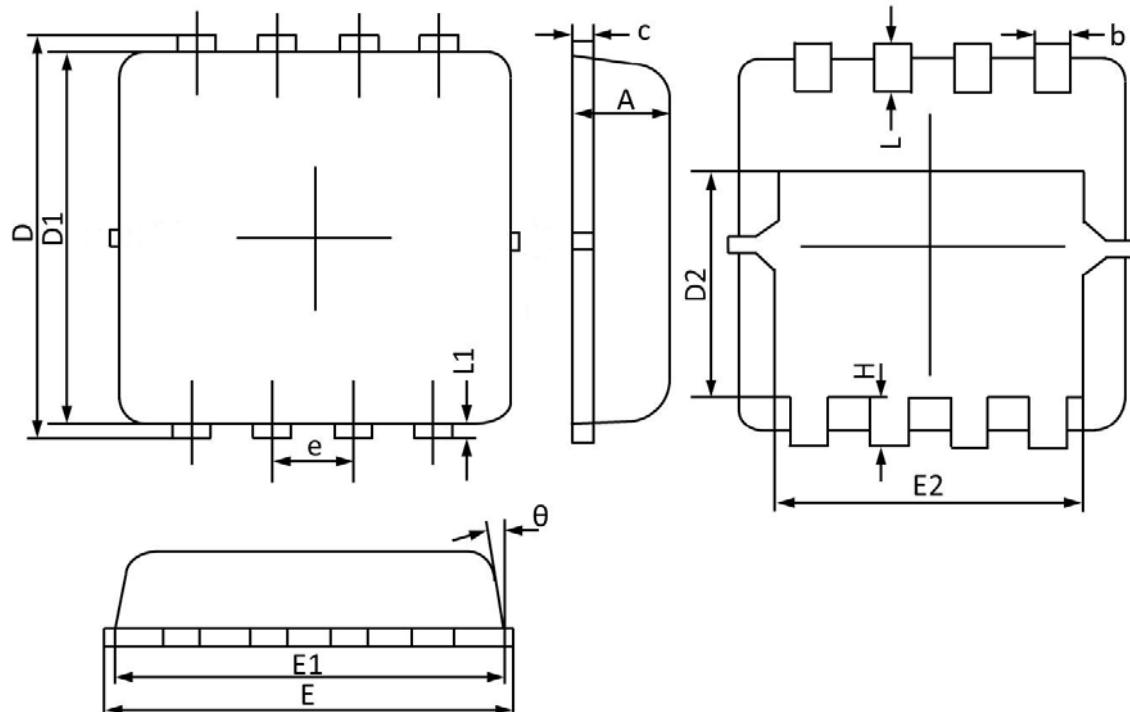


Fig.8 Gate Charge Waveform

PACKAGE OUTLINE & DIMENSIONS

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PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.900	0.028	0.035
b	0.240	0.350	0.009	0.014
c	0.100	0.250	0.004	0.010
D	3.050	3.450	0.120	0.136
D1	2.900	3.200	0.114	0.126
D2	1.350	1.850	0.053	0.073
E	3.000	3.400	0.118	0.134
E1	2.900	3.250	0.114	0.128
E2	2.350	2.600	0.093	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.012	0.020
L	0.300	0.500	0.012	0.020
L1	0.070	0.200	0.003	0.008
θ	0°	12°	0°	12°